

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
ADETUTU, OLUBUNMI O., et al.

Application No.: 10/827202

Filed: 04-19-2004

Docket No.: SC13238TP

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Patent No.: 7030001 B2

Issue Date: 04-18-2006

Examiner: Caridad Everhart

Group Art Unit: 2891

Title: METHOD FOR FORMING A GATE ELECTRODE HAVING A METAL

Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. §1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

March 6, 2008
Date of Submission

Teresa Shephard
Signature

Teresa Shephard
Printed Name of Person Signing Certificate

Commissioner for Patents
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the errors appear to be on the part of the United States Patent Office, there should be no charge.

3/6/08
Date

Respectfully submitted,

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CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO.: 7030001 B2
APPLICATION NO: 10/827202
DATE: 04-19-2004
FIRST NAMED INVENTOR: ADETUTU, OLUBUNMI O.

It is certified that error appears in the above-identified patent and that said Letters
Patent are hereby corrected as shown below:

In Column 10, Line 2, Claim No. 34:

Change "fanning" to --forming--

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc.
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